Roll No ....

MEVD-301(B)
M.E./M.Tech. III Semester
Examination, June 2014
System On Chip (SOC) Design (Elective-IV)
Time : Three Hours

Maximum Marks : 70

Note: Attempt any five questions out of eight.

1. Explain the functions of (FPGA's) using Algotronix corporate features. Also explain different advance technologies which is used in semiconductor technology to grow its market.

2. Write down the different applications of embedded system which pressure the system level hardware-software co-design.

3. Write down the program for multiplier 2×2, which performs the multiplication of two 2-bit numbers and stores the product in a 4-bit register.

4. Write down the various steps involved in design flow of any program entity using Xilinx ISE 10.1. Also explain its functions.

5. Explain the block diagram of Development Board Architecture displaying the FPGA Board. Also explain the functions of each block in peripheral interfacing.

6. Write down the consequences and properties of any kind of ARM system on-chip architecture. Also mention how this kind of architecture helps to grow architectural design.

7. Explain simulation in HDL. Also write down the steps which verifies simulation in model sin.

8. Write short notes (any two):
   a) CISC / RISC
   b) MMU
   c) Single programmable chip
   d) HDL operation

******