Total No. of Questions : 8] *[Total No. of Printed Pages : 2*

Roll No .....

EE/EX-3004 (CBGS)

B.E. III Semester

Examination, December 2017

Choice Based Grading System (CBGS)

Analog Electronics

Time : Three Hours

Maximum Marks : 70

Note: i) Attempt any five questions.
ii) All questions carry equal marks.

1. a) Discuss the principle working of zener diode and tunnel diode.
   b) Explain the working of full wave rectifier.

2. a) Discuss the working of transistor as an amplifier.
   b) Discuss small signal analysis of transistor using h-parameters.

3. a) Discuss about thermal runway and thermal stability.
   b) Explain the working principle of FET.

4. a) What is negative feedback? What are its merits.
   b) Explain the working of R-C phase shift oscillator.

5. a) Discuss the working of class-B amplifier and calculate its efficiency.
   b) Explain the working of push pull amplifiers and its benefits.

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6. a) Draw the circuit diagram of bistable multivibrator and explain its working.
   b) Explain the working of differential amplifier.

7. a) Explain the working of op-amp as differentiator and integrator.
   b) Explain the working of op-amp as log and antilog amplifier.

8. Write short notes on any two of the following:
   a) Hartley and Colpitts oscillator
   b) Darlington pair
   c) Schmitt trigger

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