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IT - 713

B.E. VII Semester

Examination, December 2015

Automata and Compiler Design

Time: Three Hours

Maximum Marks: 70

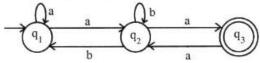
Note: i) Answer five questions. In each question part A, B, C is compulsory and D part has internal choice.

ii) All parts of each question are to be attempted at one place.

- iii) All questions carry equal marks, out of which part A and B (Max. 50 words) carry 2 marks, part C (Max. 100 words) carry 3 marks, part D (Max. 400 words) carry 7 marks.
- iv) Except numericals, Derivation, Design and Drawing etc.
- What are the applications of FA?
 - Construct a DFA that accept the set of all strings ending with 111 over alphabet $\{0, 1\}$.
 - c) Write the identity rules for regular expression.
 - Give regular expressions for the following over {a, b}
 - i) Each string starts an ends with same symbol.
 - ii) Length of the strings is atleast three.
 - iii) The number of b's is exactly two.

OR

Construct the regular expression for the DFA shown in fig.



- Explain parser tree with example.
 - Write a short note on context free grammar.
 - Give a brief description about LEX.
 - Verify whether the following grammar is LL(1) or not $S \rightarrow iEtSS/a$

 $S' \rightarrow es/\Lambda$

 $E \rightarrow b$

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OR

Check whether the following grammar is LL(1).

S→AaAb/BbBa

 $A \rightarrow \wedge$

 $B \rightarrow \wedge$

- Give the parse tree and translations for the expression (4*7+19)*2 according to syntax directed translation.
 - What is the significance of syntax directed definition?
 - Compare SLR, canonical LR, LALR.
 - Translate the expression (a+b)*(c+d) + (a+b+c) into
 - Quadruples
 - ii) Tripes
 - iii) Indirect tribles

OR

Construct LALR parsing table for the following grammar S → Aa/bAc/dc/bda

 $A \rightarrow d$

- What is an activation tree? 4. a)
 - Explain syntactic phase error. b)
 - Explain static scoping and dynamic scoping. c)
 - Compare and contrast various allocation strategies.

OR

Explain error detection and recovery.

- Write a brief note on peephole optimization. 5. a)
 - Explain data flow analysis of flow graphs. b)
 - Explain register allocation and assignment. c)
 - Describe various machine independent code optimization techniques.

OR

Describe the various issue in the design of a code generator.

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